



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

NAVEED MAJID ET AL

PHA 23,843

Serial No. 09/440,595

Group Art Unit: 2811

Filed: November 15, 1999

Examiner: N. Parekh

Title: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN  
HIGH-POWER APPLICATIONS

Commissioner for Patents  
Washington, D.C. 20231

AMENDMENT

Sir:

In response to the Official Office Action dated February 14,  
2001, please amend the above-identified application as follows:

IN THE TITLE

Please cancel the present title and substitute therefor the  
following title:

MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN  
POWER APPLICATIONS

IN THE SPECIFICATION

**Page 1, in the paragraph beginning on line 1, change as follows:**

The invention is in the field of semiconductor devices, and  
relates more specifically to multiple semiconductor chip

2811  
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MAY 17 2001  
TECHNOLOGY CENTER 2800  
#051801  
5/18/01